

IMPORTANT SL10 TECHNICAL ADDENDUM

IMPORTANT NOTICE

Although your SL10 is equipped with a relay to suppress pulses generated by interrupted regulation, it is conceivable that a partial wave transient could pass prior to suppression.

For this reason, it is strongly recommended that your SL10 remain connected to an operating power source at all times. It has been designed for this constant stand-by condition, without compromising the life expectancy or performance of its components, and exhibits a minuscule energy draw in this mode.

If it is necessary to turn your SL10 on and off always make certain to turn on your SL10 before your amplifier, and turn off your amplifier before your SL10.

SPECIFICATION ADDENDUM

The technical data appearing in your owner's manual was obtained from design prototypes in order to meet printing deadlines for initial product shipments. All production units of the SL10 contain minor refinements which are reflected in the following data. Please refer to this material for any technical specification you may require which now conforms to the applicable IHF measurement standards.

SL10 TECHNICAL DATA

DESCRIPTION: Two channel, low level signal processing unit having switch selected capacitance or impedance load characteristics for matching all velocity characteristic cartridges. Built-in preliminary gain phono stage, operated without feedback, for moving coil cartridges. Three high level, amplitude characteristic (flat) inputs. Record/monitor facilities for a single tape recorder. Front panel control functions consist of program selection, instantaneous source or recorder monitoring, channel balance, and audio level. Audio processing circuits are powered from a separate power supply module and operate with a total of 20,000 microfarads capacitive regulation throughout the supply circuitry.

Audio circuits incorporate ultra-fast, cascode/class A, direct coupled operation. High gain, high speed (200,000,000 Hz bandwidth) semiconductors are individually selected through noise and curve-trace linearity analysis. Auto-null insures dc offset of no more than ± 10 millivolts maximum at the output, even when injected at the input by associated equipment. Superior power supply rejection is achieved through quadruple decoupling.

SPECIFICATIONS

PRELIMINARY GAIN STAGE (all tests conducted with 100 ohm source)

frequency response: +0 dB, -3 dB, .5 Hz through 200 kHz
square wave rise time: .5 microsecond
overload point: 1 volt peak output
distortion: .03% second harmonic at 50 millivolts out
noise: (preliminary gain stage + RIAA stage) -85 dB A weighted
referenced to 1 volt output from RIAA stage

RIAA GAIN STAGE

equalization: less than .15 dB deviation 50 Hz through 20 kHz,
plus 3.5 Hz subsonic rolloff. Fully integrating curve
input overload: 280 millivolts peak at 1 kHz
distortion: less than .01% 10 Hz through 20 kHz, .025% at 20 Hz
noise: -90 dB, A weighted referenced to 1 volt output

HIGH LEVEL STAGE

frequency response: +0 dB, -3 dB dc through 200 kHz
square wave rise time: less than .6 microseconds
internal circuit delay: input to output transit time. less than
.02 microseconds
slew rate: 150 volts/microsecond (resistive load)
IHF IM distortion: .01% at 5 volts output into 10 kOhm, 1 nano-
farad load
maximum output before clipping: ± 10 volts peak

Because Threshold is constantly researching new technology and materials, the option is reserved to incorporate design refinements and modifications into existing product lines without notice or obligation.